IN THE CLAIMS:

Please amend the claims as follows:

Claim 1 (Currently Amended): A liquid crystal display including liquid crystal pixel cells arranged at each intersection between a plurality of gate lines and a plurality of data lines, comprising:

a thin film transistor associated with each pixel cell; a storage capacitor; and

a smectic liquid crystal between an upper substrate and a lower substrate, wherein the smectic liquid crystal has spontaneous polarization in a range of 2nC/cm² to 10nC/cm² 70nC/cm² and a unit storage capacitance is in a range of 1nF/cm² to 4.5nF/cm² 7nF/cm² for optimizing transmittance depending on the spontaneous polarization of the smectic liquid crystal.

Claims 2-3 (Cancelled).

Claim 4 (Currently Amended): A liquid crystal display including liquid crystal pixel cells arranged at each intersection between a plurality of gate lines and a plurality of data lines, comprising:

- a thin film transistor associated with each pixel cell;
- a storage capacitor; and
- a smectic liquid crystal between an upper substrate and a lower substrate, wherein the smectic liquid crystal has spontaneous polarization is in a range of 70nC/cm² to 100nC/cm² and a unit storage capacitance is in a range of 5nF/cm² to 13nF/cm² for optimizing transmittance depending on the spontaneous polarization of the smectic liquid crystal.

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Claim 5 (New): A liquid crystal display including liquid crystal pixel cells arranged at each intersection between a plurality of gate lines and a plurality of data lines, comprising:

a thin film transistor associated with each pixel cell;

a storage capacitor; and

a smectic liquid crystal between an upper substrate and a lower substrate, wherein the smectic liquid crystal has spontaneous polarization in a range of 10nC/cm² to 70nC/cm² and a unit storage capacitance is in a range of 4nF/cm² to 7nF/cm² for optimizing transmittance depending on the spontaneous polarization of the smectic liquid crystal.